

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. <b>251390US2</b>		SERIAL NO. <b>10/814,345</b>	
<div style="display: flex; align-items: center; justify-content: center;"> <div style="border: 1px solid black; border-radius: 50%; padding: 10px; text-align: center; margin: 0 10px;"> <b>PTO</b>  <b>AUG 18 2004</b>  <b>PTENT &amp; TRADEMARK OFFICE</b> </div> <div style="text-align: left;"> <b>LIST OF REFERENCES CITED BY APPLICANT</b>  <b>APPLICANT</b>  <b>Tomoaki SHINO</b>  <b>FILING DATE</b>  <b>April 1, 2004</b> </div> </div>				<b>GROUP</b>			
				<b>U.S. PATENT DOCUMENTS</b>			
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
<b>FOREIGN PATENT DOCUMENTS</b>							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES      NO		
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
<b>SWL</b>	AW	T. OHSAWA, et al., International Solid-State Circuits Conference, Digest of Technical Papers, page 153, "MEMORY DESIGN USING ONE-TRANSISTOR GAIN CELL ON SOI", 2002					
	AX						
	AY						
	AZ						<input type="checkbox"/> Additional References sheet(s) attached
Examiner <b>CRA NC</b>				Date Considered <b>8/2005</b>			
<small>*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							